



15992 U.S. PTO

PATENT APPLICATION  
Attorney's Do. No. 9903-088

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

22390 U.S. PTO  
10/802999



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Mail Stop Patent Application.  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

Enclosed for filing is a patent application under 37 CFR 1.53(b) of:

Inventor: Dae-Woon SON and Hyoung-Chan CHANG  
For: TAPE CIRCUIT BOARD AND SEMICONDUCTOR CHIP PACKAGE  
INCLUDING THE SAME

This application is a ☐ continuation, ☒ divisional, ☐ continuation-in-part of prior application Serial No. 10/059,661, filed January 28, 2002.

Prior application info: Examiner: Dinh, Tuan T. Group Art Unit 2827

Applicant requests FIG. 13 to be published with the application.

Enclosures:

- ☒ Specification (pages 1-7); claims (page 8); abstract (page 9)
- ☒ SEVEN sheet(s) of FORMAL drawings (comprises 13 Figs.)
- ☒ Declaration or Combined Declaration and Power of Attorney
- ☒ Copy from a prior application (37 CFR 1.63(d))
- ☒ Incorporation by Reference--The entire disclosure of the prior application, from which a copy of the oath or declaration is supplied is considered as being part of the disclosure of the accompanying application and is hereby incorporated by reference therein.
- ☒ Recorded Assignment (copy from prior application)  
Assignee Name and Address: Samsung Electronics Co., Ltd.  
Suwon-city, Kyungki-do, Republic of Korea
- ☒ Information Disclosure Statement with Form PTO 1449
- ☒ Return Postcard

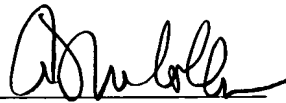
<u>CLAIMS AS FILED</u>				
For	Number Filed	Number Extra	Rate	Basic Fee \$770
Total Claims	5-20	0	x \$ 18 =	0
Independent Claims	1-3	0	x \$ 86 =	0
TOTAL FILING FEE				\$770

- ☒ PTO Form 2038 authorizing credit card payment for the above-listed fees is enclosed.  
☒ Any deficiency or overpayment should be charged or credited to deposit account number 13-1703. A duplicate copy of this sheet is enclosed.

**Customer No. 20575**

Respectfully submitted,

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